



**Sterne Kessler
Goldstein Fox**
ATTORNEYS AT LAW

Robert Greene Sterne
Jorge A. Goldstein
David E. S. Connell
Robert W. Fennell
Tracy Gene G. Durkin
Michelle A. Cirtella
Michael B. Ray
Robert E. Spada
Eric C. Turley
Michael O. Lee
John M. Covert
Robert C. Milling
Donald J. Featherstone
Dorothea J. Shea, Jr.
Michael V. Messinger
Judith U. Kim
Mark Fox Evans
Jeffrey L. Helweg
Edison L. Elison
Donald R. Bassett
Peter A. Achman
Brian J. Del Buono

Elizabeth J. Haines
Michael D. Specter
Kevin W. McCabe
Glenn J. Perry
Theodore A. Wood
Gail L. Longworth
Edward W. Yen
Grant E. Reed
Jason D. Eisenberg
Tracy L. Muller
Jon L. Wright
Lawrence M. Desantis
Helen C. Carlson
Cynthia M. Bouchez
Timothy A. Doyle
Lori A. Gordon
Shannon A. Carroll
Julie F. Flint
Michelle K. Holoubek
Martha B. Rosen
Scott A. Schaller
Lee Zhou

W. Blake Cochran
James J. Ford
John I. Haran
Mark W. Byrd
Michael R. Golek
Cathy A. Kim
Doyce A. Sever
Julie Winkler Jenkins
Paul A. Calvo
C. Matthew Rieker
Randall E. Baldwin
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Jeffrey K. Mills
Mika Matherice
Scott M. Woodhouse
Peter A. Scamarcio
Christian A. Camarce
Richard D. Collier
Patrick P. Hansen
Ross G. Hicks

Kerith Hyton-Rodic
Gemma A. Lang
Bernie Nantiergo Combs
Alissa K. Sandorwitz
Jonathan M. Strong
John P. Wernickoff
Chenghua Luo
Salvador M. Bezos
Bazee B. Vance
Justin L. Shee
Byron L. Pichard
Kellie K. DiNapoli
Richard A. Adams
Christopher B. Forster
Jeffrey H. Fougere
William F. Judd
Registered Patent Agents
Karen R. Markovitz
Dannelle L. Jeffery
Steven C. Oppenheimer
Aaron S. Lukas

Gaurav Asthana
Jesse Mourada
Cynthia C. DeRenzo
Uma F. Jandt
Basil W. Powers III
Erik C. Weiss
Joseph E. Mitschellhaus
Karon Nasabzadeh
Aaron S. Ward
PT Counsel
Edward J. Kessler
Berneth C. Barr III
Christopher P. Wrist
David C. Isaacson

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WRITER'S DIRECT NUMBER:
(202) 772-8512
INTERNET ADDRESS:
IPOHL@SKGF.COM

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Art Unit 2117

Attn: Certificate of Correction Branch

Re: U.S. Utility Patent
Patent No. 7,373,561 B2; Issued: May 13, 2008
For: **Integrated Packet Bit Error Rate Tester for 10G SERDES**
Inventors: Baumer *et al.*
Our Ref: 1875.4430000

Sir:

Transmitted herewith for appropriate action are the following documents:

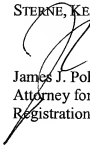
1. Request for Certificate of Correction Under 37 C.F.R. 1.322; and
2. Certificate of Correction (Form PTO/SB/44).

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.


James J. Pohl
Attorney for Patentees
Registration No. 60,724

JJP/la
Enclosure(s)